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DESCRIPTION

MULTILAYER CERAMIC DEVICE AND THE PRODUCTION METHOD
THEREOF

5 [0001]

TECHNICAL FIELD

[0002]

The present invention relates to a production method of a multilayer ceramic device, such as a multilayer ceramic capacitor, and particularly relates to a production method of a multilayer ceramic device, wherein an effect of a level difference is reduced by burying a gap arising on an electrode pattern layer when producing a ceramic green sheet multilayer body with a level difference absorbing blank pattern layer.

BACKGOUND ART

[0003]

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For example, a multilayer ceramic capacitor is 20 produced by the procedure below.

First, dielectric slurry obtained by dispersing inorganic colorant powder (ceramic dielectric powder) in a solvent including a nonvolatile organic component, such as a dispersant, polymeric resin and plasticizer, is produced. Next, the dielectric slurry is applied to a

plastic support film by the doctor blade method, nozzle method or other means and dried to obtain a dielectric green sheet.

[0004]

Next, an electrode pattern layer is formed on the dielectric green sheet. The electrode pattern layer is generally formed by performing screen printing of an electrode paste.

[0005]

Next, the dielectric green sheet including the electrode pattern layers is removed from the support base film, cut to be a predetermined size, then, stacked for a plurality of times while aligning pattern positions of the electrode pattern layers, pressured and bonded, so that a ceramic green multilayer body is obtained. Next, the multilayer body is cut to be a predetermined size to form a chip, then, fired at a predetermined temperature and atmosphere. The thus obtained fired body chip is applied with external electrodes and burnt at its end portions, so that a multilayer ceramic capacitor is completed.

[0006]

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In the production procedure of the multilayer ceramic capacitor as above, when forming an electrode pattern layer in a predetermined pattern on the

dielectric green sheet, there is a level difference gap
blank portion, on which the electrode pattern layer dose
not exist. Due to the level difference gap portion, a
level difference is formed on a surface of the

5 dielectric green sheet. A number of the electrode
pattern layers with the level difference gap portion are
stacked via green sheets. Since the stacked body is
pressured and bonded after that, the level difference
gap portions are crushed. Therefore, the larger the

10 number of stacked layers of the multilayer body and the
thinner a thickness of the green sheet is, the larger
the effects of the accumulated level difference becomes.

[0007]

As a result, a green sheet sandwiched by the

portions of the electrode pattern layers is tightly

pressured and bonded to obtain a high density, while a

density of a green sheet sandwiched by portions having

the level difference gap portions becomes lower

comparing with that on other portions, so that a density

difference arises in the multilayer ceramic body. Also,

there arises a disadvantage that adhesiveness with the

upper and lower green sheets declines in the green sheet

sandwiched by the portions with the level difference gap

portions.

25 [0008]

The multilayer body is cut to be a chip after that, then, fired, but when a multilayer body having the above problem is fired, it easily cracks between layers, which has to be overcame. Also, there is a problem that structural defects, such as chip deformation, a short-

structural defects, such as chip deformation, a shortcircuiting defect, cracks and delamination, often arise after firing the multilayer body.

[0009]

To solve the above problems, for example as shown
in the patent articles 1 to 5 below, methods of burying
a level difference gap portion with a blank pattern
layer generated by printing an electrode paste have been
proposed. According to these methods, a surface
including an electrode layer can be made flat and the
problems in the ceramic capacitor due to a level
difference as explained above can be improved.

[0010]

Patent Article 1: The Japanese Unexamined Patent Publication No. 56-94719

20 Patent Article 2: The Japanese Unexamined Patent Publication No. 3-74820

Patent Article 3: The Japanese Unexamined Patent Publication No. 9-106925

Patent Article 4: The Japanese Unexamined Patent

25 Publication No. 2001-126951

Patent Article 5: The Japanese Unexamined Patent Publication No. 2001-358036

DISCLOSURE OF THE INVENTION

5 [0011]

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In recent years, multilayer ceramic capacitors are demanded to have a larger number of layers and a larger capacity. Therefore, there has been proposed an attempt of making a thickness of the green sheet 3µm or thinner (2.5µm or thinner as an interlayer thickness after firing).

[0012]

However, the present inventors have found that, when a thickness of the green sheet is made as thin as 3µm or thinner, a problem is newly caused by the weak adhesiveness on a contact surface between the green sheet and the dielectric blank pattern layer in the technique of the related art explained above. For example, dielectric green sheets deviate from one another when stacked, so that positional deviation easily arise between patterns of the upper and lower electrode layers, and the positional deviation accumulates as the layers increase, which results in a problem of causing a cutting defect in the cutting step. Also, there arises a problem that stacking deviation and

cracks on the multilayer body easily arise in the cutting step.

[0013]

Furthermore, since a volume shrinkage amount in

the subsequently performed binder removal step and
firing step slightly differs between the ceramic green
sheet, electrode pattern layer and level difference
absorbing dielectric blank pattern layer, the contact
surface is unable to endure a generating stress, and

there also arises a problem that chaps and cracks easily
arise along the boundary.

[0014]

The present invention was made in consideration of the above circumstances and has as an object thereof to provide a production method of a multilayer type electronic device capable of producing a multilayer ceramic device, such as a multilayer ceramic capacitor having a high capacity, wherein an interlayer thickness is made as thin as about 2.5µm or thinner, at a high production yield without causing unsticking between layers and internal defects, etc.

[0015]

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To attain the above object, a production method of a multilayer ceramic device according to the present invention comprises the steps:

forming a green sheet by using a green sheet slurry;

forming an electrode pattern layer on the green sheet;

forming a dielectric blank pattern layer on level difference gap portion on the green sheet so as to bury the level difference by using dielectric paste;

forming a multilayer body by stacking a plurality of stacking units, wherein the stacking unit is the green sheet formed with the dielectric blank pattern layer and the electrode pattern layer; and

firing the multilayer body;

wherein:

the green sheet slurry includes a first inorganic

dielectric colorant powder and a first organic binder

component;

the electrode level difference absorbing dielectric paste includes a second inorganic dielectric colorant powder and a second organic binder component;

20 and

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when a first weight ratio of the first organic binder component with respect to the first inorganic dielectric colorant powder in the green sheet slurry is (A), and a second weight ratio of the second organic binder component with respect to the second inorganic

dielectric colorant powder in the electrode level difference absorbing dielectric paste is (B),

the second weight ratio (B) is larger than the first weight ratio (A).

5 [0016]

The green sheet is, for example, a part to be a dielectric layer composing a capacitance of a capacitor after firing. When the first weight ratio (A) of the first organic binder component with respect to the first inorganic dielectric colorant powder in green sheet slurry composing the green sheet is too high, a density of the first inorganic dielectric colorant powder becomes too low and a variety of disadvantages tend to arise.

15 [0017]

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On the other hand, the dielectric blank pattern layer is a part not affecting short-circuiting, etc. of the capacitor after firing. Also, the dielectric blank pattern layer has a smaller occupying area comparing with that of the green sheet. Accordingly, there are not any problems even when the second weight ratio (B) of the second organic binder component with respect to the second inorganic dielectric colorant powder in the electrode level difference absorbing dielectric paste composing the dielectric blank pattern layer is higher

than the first weight ratio (A).

[0018]

In fact, by making the second weight ratio (B) higher than the first weight ratio (A), adhesiveness between the green sheet and the dielectric blank pattern layer contacting each other in the stacking direction can be improved. Note it was first found by the present inventors that adhesiveness of a contacting surface of the green sheet and the dielectric blank pattern layer could be improved by increasing an amount of the organic binder component in the electrode level difference absorbing dielectric paste to be larger than that of the organic binder component in the green sheet slurry.

[0019]

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In the present invention, it is possible to improve adhesiveness between the green sheet and the dielectric blank pattern layer even when producing a multilayer ceramic device, such as a multilayer ceramic capacitor having a high capacity, wherein particularly a thickness of the green sheet is made as thin as 3µm or thinner (a thickness between layers in the fired stacked body is 2.5µm or thinner).

[0020]

Therefore, green sheets do not deviate when

25 stacked, positional deviation of patterns of the upper

and lower electrode layers is hard to be caused, the positional deviation is not accumulated even if the layers increase, and cutting defects are unlikely to be caused in the cutting step of the multilayer body. Also, stacking deviation and cracking of the multilayer body, etc. are hard to be caused. Furthermore, also in the subsequently performed binder removal step and firing step, chaps and cracks along boundaries of the multilayer body are hard to be caused. Accordingly, in the present invention, it is possible to produce a multilayer ceramic device at a high production yield without causing any unsticking between layers and internal defects, etc.

[0021]

15 Furthermore, in the present invention, the dielectric blank pattern layer is formed on the level difference gap portion in the electrode pattern layer and a surface of the green sheet having electrode pattern layer is made flat without any level differences, so that disadvantages at the time of stacking or after firing caused by the level differences can be eliminated.

[0022]

The first organic binder component and/or the second organic binder component are composed of a polymeric resin and a plasticizer.

[0023]

Preferably, the second weight ratio (B) in the electrode level difference absorbing dielectric paste is 5 to 40 wt%. When the ratio is too small, the effect of the present invention becomes small, while too large, an excessive plasticizer eludes on the printing surface or soaks into the dielectric green sheet to easily deteriorate the sheet property. Furthermore, an organic component amount with respect to the dielectric inorganic colorant become relatively large and a density of the inorganic dielectric colorant powder reduces, so that it easily leads to problems that time required by binder removal becomes long, volume shrinkage increases in the binder removal step, the electrode layers deform, and cracks increase.

[0024]

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Preferably, a weight ratio of the polymeric resin is 10 wt% or less with respect to the inorganic dielectric colorant powder. Since the polymeric resin exists as a solid content in the film of the dielectric blank pattern layer after removing plasticizer, when it exceeds 10 wt%, a density of the inorganic dielectric colorant powder becomes low to result in a high tendency of causing an increase of volume shrinkage in the residual binder removal step, deformation of electrode

layers and increase of cracks.

[0025]

Preferably, a value (B-A) obtained by subtracting the first weight ratio (A) from the second weight ratio (B) is 1.5 or larger. When (B-A) is too small, the effect of the present invention becomes small.

[0026]

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The multilayer ceramic device according to the present invention is obtained by any one of the above production methods. In the present invention, the multilayer ceramic device is not particularly limited and a multilayer ceramic capacitor and a multilayer inductor, etc. may be mentioned.

15 BRIEF DESCRIPTION OF DRAWINGS

[0027]

FIG. 1 is a schematic sectional view of a multilayer ceramic capacitor obtained by a production method according to an embodiment of the present invention.

FIG. 2 is a sectional view of a key part showing a step of producing the multilayer ceramic capacitor shown in FIG. 1.

FIG. 3 is a schematic sectional view showing a 25 step continued from FIG. 2.

BEST MODE FOR CARRYING OUT THE INVENTION [0028]

Below, the present invention will be explained based on embodiments shown in the drawings.

First, as an embodiment of an electronic device according to the present invention, an overall configuration of a multilayer ceramic capacitor will be explained.

[0029] 10

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As shown in FIG. 1, the multilayer ceramic capacitor 2 according to the present embodiment has a capacitor element body 4, a first terminal electrode 6 and a second terminal electrode 8. The capacitor element body 4 has dielectric layers 10 and electrode layers 12, 15 and the electrode layers 12 are stacked alternately between the dielectric layers 10. One side of the alternately stacked electrode layers 12 is electrically connected to inside of a first terminal electrode 6 formed outside of the first terminal portion of the capacitor element body 4. While, the other side of the alternately stacked electrode layers 12 is electrically connected to inside of the second terminal electrode 8 formed outside of a second end portion of the capacitor 25 element body 4.

[0030]

A material of the dielectric layer 10 is not particularly limited and composed of a dielectric material, such as calcium titanate, strontium titanate and/or barium titanate. A thickness of each of the dielectric layers 10 is not particularly limited, but generally several µm to hundreds of µm. Particularly in the present embodiment, it is made as thin as preferably 5µm or thinner, more preferably 2.5µm or thinner, and particularly preferably 1.5µm or thinner.

[0031]

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A material of the electrode layer 12 is not particularly limited and it is composed of nickel, a nickel alloy, silver, palladium, copper, a copper alloy, or other metal or an alloy. A thickness of the internal electrode layer 12 is not thicker than that of the dielectric layer 10.

[0032]

Also, a material of the terminal electrodes 6 and 8 is not particularly limited, but normally copper, a copper alloy, nickel and a nickel alloy, etc. are used. Silver or an alloy of silver and palladium, etc. may be also used. A thickness of the terminal electrodes 6 and 8 is not particularly limited and is normally 10 to 50 pm or so.

[0033]

A shape and size of the multilayer ceramic capacitor 2 may be suitably determined in accordance with the use object. When the multilayer ceramic capacitor 2 is rectangular parallelepiped, the size is normally a length (0.6 to 5.6 mm, preferably 0.6 to 3.2 mm) × a width (0.3 to 5.0 mm, preferably 0.3 to 1.6 mm) × a thickness (0.1 to 1.9 mm, preferably 0.3 to 1.6 mm) or so.

10 [0034]

Next, an example of a production method of the multilayer ceramic capacitor 2 according to the present embodiment will be explained.

[0035]

15 First, to produce a ceramic green sheet to compose the dielectric layers 10 shown in FIG. 1 after firing, dielectric slurry (green sheet slurry) is prepared.

The dielectric slurry is formed by dispersing a dielectric inorganic material (ceramic powder/the first inorganic dielectric colorant powder) in an organic solvent with an organic vehicle.

[0036]

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As the dielectric inorganic material, it is not particularly limited and a composition group including a variety of inorganic additives may be suitably selected

to realize functions of a temperature compensation material and a high permittivity based material in addition to barium titanate, lead-containing perovskite and alumina, etc. These materials may be suitably selected from a variety of compounds, which become composite oxides or oxides, such as carbonate, nitrite, hydroxide and organic metal compound and mixed to be used. The dielectric material is normally used as powder having an average particle diameter of 0.4µm or smaller, and preferably 0.1 to 3.0µm or so. Note that it is preferable to use a smaller powder comparing with a green sheet thickness to form an extremely thin green sheet.

[0037]

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The organic vehicle is obtained by dissolving an organic binder component in an organic solvent. The organic binder component indicates a polymeric resin as a binder resin or a polymeric resin and plasticizer.

[0038]

20 The organic solvent used for the organic vehicle is not particularly limited and acetone, toluene, methylethyl ketone, methyl alcohol, ethyl alcohol, isopropyl alcohol, xylene and other organic solvent may be used.

25 [0039]

The polymeric resin to be used as the organic vehicle is not particularly limited and a cellulose based resin including a variety of cellulose derivatives, such as cellulose ester and cellulose ether; an acetal resin; butyral resin; an acrylic based resin obtained by polymerizing an acrylic resin and a derivative thereof; a methacrylate based resin obtained by polymerizing methacrylate and a derivative thereof; an olefin based resin including a variety of copolymers with ethylene or propyrene and vinyl acetate, acrylic acid, acrylic ester, methacrylate, methacrylate ester, glycidic acid, glycidic acid ester, etc.; an urethane resin and an epoxy resin, etc. may be mentioned. One or more kinds may be suitably selected from them.

15 [0040]

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The plasticizer is not particularly limited and dimethyl phthalate, diethyl phthalate, dibutyl phthalate, dioctyl phthalate, benzylbutyl phthalate and other phthalate ester, aliphatic dibasic acid ester,

20 phosphorous ester, etc. may be used.

[0041]

The organic binder component (a polymeric resin + plasticizer) in the dielectric slurry is preferably 3 to 16 wt% with respect to a dielectric inorganic colorant powder, and an adding quantity of the plasticizer is

preferably not more than 100 wt% with respect to the polymeric resin. When the organic binder component is 3 wt% or less, it is liable that an effect of the bonding each inorganic dielectric colorant powder becomes reduced, powder of the inorganic colorant easily comes off from the green sheet, and strength of the sheet deteriorates. Also, when the organic binder component exceeds 16 wt%, the organic component amount becomes relatively large with respect to the dielectric inorganic colorant powder, consequently, time required 10 for the binder removal becomes long and a density of the dielectric inorganic colorant powder in the green sheet becomes low, so that volume shrinkage in the binder removal step increases, which tends to lead to problems of a decline of final chip size accuracy, deformation of 15 electrode layers and an increase of cracks. While, when the plasticizer amount exceeds 100 wt%, it is liable that strength of the dielectric green sheet declines and defects in the sheet increase because removing from the support film becomes difficult, etc. 20

[0042]

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In the dielectric slurry, a variety of dispersants, antistatic agents, release agents or other additives may be added in accordance with need. Note that a total content thereof is preferably 10 wt% with respect to the

inorganic dielectric colorant powder.

[0043]

Next, by using the above dielectric slurry, a green sheet 10a is formed to be a thickness of

5 preferably 3.0µm or thinner and more preferably 0.5 to
2.5µm or so on a carrier sheet 20 as a support sheet as shown in FIG. 2 by the doctor blade method, etc. The green sheet 10a is dried after forming the carrier sheet 20. A drying temperature of the green sheet 10a is preferably 50 to 100°C, and the drying time is preferably 1 to 5 minutes.

[0044]

As the carrier sheet 20, for example, a PET film, etc. is used and those coated with silicon, etc. for improving releasability is preferable. A thickness of these carrier sheets 20 is not particularly limited and preferably 5 to 100µm.

[0045]

Next, in the present embodiment, as shown in FIG.

20 2, an electrode pattern layer 12a having a predetermined pattern and a dielectric blank pattern layer 24 having substantially the same thickness as that of the electrode pattern layer 12a are formed on the surface of the green sheet 10a by a printing method or a transfer method, etc. In the explanation below, a method of

forming an electrode pattern layer 12a having a predetermined pattern and a dielectric blank pattern layer 24 by a screen printing method or a gravure printing method.

5 [0046]

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First, an electrode paste is prepared. The electrode paste is fabricated by kneading a conductive material composed of a variety of conductive metals and alloy, a variety of oxides, which becomes the above conductive materials after firing, organic metal compounds, or resinates, etc. with an organic vehicle.

[0047]

As the conductive material to be used for producing an electrode paste, Ni, a Ni alloy or a mixture of these may be used. A shape of the conductive material is not particularly limited and may be a spherical shape or a depressed shape, etc. or a mixture of these shapes. Also, as to an average particle diameter of the conductive material, normally those having 0.05 to 1µm and preferably 0.1 to 0.5µm or so may be used.

[0048]

As the organic vehicle for the electrode paste, the same organic vehicles as those for the electrode level difference absorbing dielectric paste are used.

[0049]

After or before printing the electrode pattern layer in a predetermined pattern on the surface of the green sheet 10a by a printing method using electrode paste, a dielectric blank pattern layer 24 having substantially the same thickness as that of the electrode pattern layer 12a is formed on the surface of the green sheet 10a not having the electrode pattern layer 12a by a printing method using electrode level difference absorbing dielectric paste. Namely, the dielectric blank pattern layer 24 is formed on level difference gap portions on the electrode pattern layer 12a to bury level differences of the electrode pattern layer 12a having a predetermined pattern.

15 [0050]

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The dielectric blank pattern layer 24 shown in FIG. 2 can be formed on the surface of the green sheet 10a by a printing method, etc. using the electrode level difference absorbing printing method. When forming the dielectric blank pattern layer 24 (FIG. 2) on the surface of the green sheet 10a by a screen printing method, it is as explained below.

[0051]

First, an electrode level difference absorbing
25 dielectric paste is prepared. The electrode level

difference absorbing dielectric paste is obtained by kneading a second inorganic dielectric colorant powder (ceramic powder) with an organic vehicle.

[0052]

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As the dielectric material (the second inorganic dielectric colorant powder) used when producing the electrode level difference absorbing dielectric paste is produced by using the same dielectric particle as that in the dielectric composing the green sheet 10a. The electrode level difference absorbing dielectric paste includes the dielectric powder and an organic vehicle.

[0053]

As organic binder component (polymeric resin + plasticizer) and a variety of additives in the electrode level difference absorbing dielectric paste, those used in the green sheet slurry may be used. Note that they do not have to be completely the same as those used in the green sheet slurry and may be different from them. Also, as a solvent composing the organic vehicle, terpineol, dihydroterpineol, diethylene glycol monobutylether, diethylene glycol monobutylether acetate or other high boiling point solvent may be used.

[0054]

In the present embodiment, a ratio of an organic binder component (the second wt% (B)) with respect to

the dielectric inorganic colorant powder in the electrode level difference absorbing dielectric paste is larger than the ratio of organic binder component (the first wt% (A)) with respect to the dielectric inorganic colorant powder in the green sheet slurry. For example, B-A \geq 1.5. Also, a ratio of the polymeric resin with respect to the dielectric inorganic colorant powder in the electrode level difference absorbing dielectric paste is preferably not more than 10 wt%.

10 [0055]

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The polymeric resin exists as solid content in a dried film of the paste, a density of the inorganic dielectric colorant powder decreases when exceeding 10 wt%. Consequently, it is liable that volume shrinkage in the binder removal step increases, the electrode layer deforms and cracks increase. On the other hand, a ratio of the organic binder component with respect to the dielectric inorganic colorant powder in the electrode level difference absorbing dielectric paste is

[0056]

When 5 wt% or less, strength of the dielectric blank pattern layer 24 for level difference absorbance becomes weak, powder of the inorganic dielectric colorant easily comes off, and adhesiveness with the

dielectric green sheet declines when stacking, so that stacking defects or other problems tend to arise. While when 40 wt% or more, an excessive plasticizer eludes on the printing surface or soaks on the dielectric green sheet side to deteriorate the sheet property.

Furthermore, an organic component amount with respect to the dielectric inorganic colorant become relatively large and a density of the inorganic dielectric colorant powder reduces, so that it easily leads to problems that time required by binder removal becomes long, volume

electrode layers deform, and cracks increase.

shrinkage increases in the binder removal step, the

[0057]

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15 layer 12a and the dielectric blank pattern layer 24 formed, for example, by assuming their stacked structure as one stacking unit U1, a plurality of the stacking units U1 may be stacked as shown in FIG. 3 to form a multilayer body 30. As shown in FIG. 3, when stacking the stacking units U1, the electrode layers 12a in the stacking units U1 stacked to be adjacent to each other are stacked to have an alternate positional relationship.

[0058]

After completing the multilayer body 30, the
25 multilayer body 30 is cut along a cutting line X to be

the capacitor body 4 shown in FIG. 1 after firing. Note that a little thick external green sheet without an electrode layer is stacked on top and at bottom in the stacking direction of the actual multilayer body 30.

5 [0059]

The cut green chip is subjected to a binder removal processing and firing processing, then, thermal treatment is performed thereon for reoxidizing the dielectric layers.

10 [0.060]

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The binder removal processing may be performed under a normal condition, but when using Ni, a Ni alloy or other base metal as the conductive material-of the internal electrode layer, it is particularly preferable to perform under the condition below.

[0061]

Temperature raising rate: 5 to 300°C per an hour Holding temperature: 200 to 600°C ---

Holding time: 0.5 to 20 hours

20 Atmosphere: in the air or wet mixed gas of N_2 and H_2

[0062]

The firing condition is preferably as below.

Temperature raising rate: 50 to 500°C per an hour

Holding temperature: 1100 to 1300°C

Holding time: 0.5 to 8 hours

Cooling rate: 50 to 500°C per an hour

Atmosphere: wet mixed gas of N_2 and H_2 , etc.

[0063]

Note that an oxygen partial pressure in an air 5 atmosphere at firing is preferably 10-2 Pa or lower, and particularly 10^{-2} to 10^{-8} Pa. When exceeding the range, the internal electrode layers tend to oxidize, while when the oxygen partial pressure is too low, it is liable that an electrode material of the internal electrode layers are abnormally sintered to be broken.

[0064]

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Thermal treatment after performing firing as above is performed with a holding temperature or highest temperature of preferably 1000°C or higher, and more preferably 1000 to 1100°C. When the holding temperature or the highest temperature at the time of thermal treatment is less than the above range, exidization of the dielectric material becomes insufficient to decline the capacity, furthermore, it reacts with the dielectric 20 substrate and the lifetime tends to become short. An oxygen partial pressure at the thermal treatment is higher than that in a reducing atmosphere at firing, and preferably 10^{-3} to 1 Pa, and more preferably 10^{-2} to 1 Pa. When less than the range, reoxidization of the 25

dielectric layer 2 becomes difficult, while when exceeding the range, the internal electrode layers 12 tends to be oxidized.

[0065]

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The thus obtained sintered body (element body 4) is subjected to end surface polishing, for example, by barrel polishing or sandblasting, etc., a terminal electrode paste is burnt thereon and the terminal electrodes 6 and 8 are plated, etc., so that the terminal electrodes 6 and 8 are formed.

The multilayer ceramic capacitor of the present invention produced as above is mounted on a print substrate, etc., by soldering, etc. And used for a variety of electronic apparatuses.

15 [0066]

In the production method of the multilayer ceramic capacitor according to the present embodiment, the second weight ratio (B) of the organic binder component with respect to the inorganic dielectric colorant powder in the electrode level difference absorbing dielectric paste is larger than the first weight ratio (A) of the organic binder component with respect to the inorganic dielectric colorant powder in the green sheet slurry. Therefore, in the multilayer body 30, adhesiveness between the green sheet 10a and dielectric blank pattern

layer 24 contacting in the stacking direction can be improved.

[0067]

As a result, in the present embodiment, even when producing a multilayer ceramic device, such as a multilayer ceramic capacitor having a high capacity, wherein particularly a thickness of the green sheet is made as thin as 3µm or thinner (a thickness between layers is 2.5µm or thinner in the multilayer body after firing), adhesiveness between the green sheet 10a and dielectric blank pattern layer 24 can be improved.

[0068]

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Accordingly, green sheets 10a do not deviate from one another at stacking, positional deviation hardly arises between patterns of the upper and lower electrode 15 layers 12a, the positional deviation does not accumulate even when the layers increase, and cutting defects are not caused in the cutting step of the multilayer body 30. Also, stacking deviation and cracks of the multilayer body, etc. are hardly caused in the cutting step. 20 Furthermore, also in the binder removal step and firing step to be successively performed thereafter, chaps and cracks along boundaries of the multilayer body 30 hardly arise. Accordingly, in the present embodiment, it is possible to produce a multilayer ceramic capacitor at a 25

high production yield without causing unsticking between layers and internal defects, etc.

[0069]

Moreover, in the present embodiment, the

5 dielectric blank pattern layer 24 is formed on the level
difference gap portions of the electrode pattern layer
12a and the surface of the electrode pattern layer 12a
is made flat without any level differences, so that
disadvantages due to level differences at stacking or
10 after firing can be eliminated.

[0070]

Note that the present invention is not limited to the above embodiments and may be variously modified within the scope of the present invention.

15 For example, the method of the present invention is not limited to a production method of a multilayer ceramic capacitor and can be applied as a production method of other multilayer type electronic devices.

20 EXAMPLES

[0071]

Below, the present invention will be explained based on furthermore detailed examples, but the present invention is not limited to the examples.

25 [0072]

Example 1

Production of Green Sheet Slurry

As a starting material, BaTiO₃ powder having an average particle diameter of 0.35 μ m (BT-02 made by Sakai Chemical Industry Co., Ltd.) was used. To attain 3 mole% of (Ba_{0.5} Ca_{0.4})SiO₃, 2 mol% of Y₂O₃, 2 mole% (in terms of MgO) of MgCO₃, 0.4 mol% (in terms of MnO) of MnCO₃, and 0.1 mole% of V₂O₅ with respect to 100 moles of the BaTiO₃ powder, wet mixing was performed by a ball mill for 16 hours, then, dried by a spray drier, so that a dielectric inorganic colorant powder was obtained.

[0073]

colorant powder, 6 parts by weight of a polyvinyl

butyral resin (PVB), 3 parts by weight of dioctyl

phthalate (DOP) as a plasticizer, 60 parts by weight of

methylethyl ketone, 40 parts by weight of ethanol and 20

parts by weight of toluene were wet mixed by a ball mill

with zirconia media having a diameter of 1 mm for 20

hours, so that a dielectric slurry (green sheet slurry)

was obtained. A ratio of organic binder component in the

dielectric slurry was 9 wt% with respect to the

dielectric inorganic colorant powder.

[0074]

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Transferred)

by adding 100 parts by weight of an organic vehicle (obtained by dissolving 5 parts by weight of an ethyl cellulose resin in 95 parts by weight of terpineol) to 100 parts by weight of Ni particles having an average particle diameter of 0.4 µm, kneading with a triple-roll to obtain an electrode paste.

[0075]

Production of Electrode Level Difference

10 Absorbing Dielectric Paste

inorganic colorant powder as that used for the green sheet slurry as explained above is added with 4.5 parts by weight of an ethyl cellulose resin as a polymeric resin, 6.3 parts by weight of benzylbutyl phthalate (BBP) as a plasticizer and 90 parts by weight of terpineol as a solvent, and wet mixed by a mixer mill with zirconia media having a diameter of 1 mm for 15 hours, so that an electrode level difference absorbing dielectric paste was obtained.

[0076]

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A ratio of organic binder component (polymeric resin + plasticizer) in the electrode level difference absorbing dielectric paste is 10.8 wt% with respect to the dielectric inorganic colorant powder, which is

larger than the organic binder component ratio of 9 wt% in the green sheet slurry as explained above by 1.8 wt%. Also, an amount of the plasticizer in the electrode level difference absorbing dielectric paste is 140 wt% with respect to the polymeric resin.

[0077]

Sample

Production of Multilayer Ceramic Capacitor

By using the green sheet slurry obtained as above,

a dielectric slurry film was formed on a carrier sheet
by a nozzle method and dried, so that a dielectric green
sheet 10a having a thickness of 3µm was obtained. On
this dielectric green sheet 10a, an electrode pattern
layer 12a having a predetermined pattern to be an
electrode layer was formed by screen printing by using
the above conductive electrode paste.

[0078]

A thickness of the electrode layer 12a after
drying was 1.5µm. Next, to bury electrode level

difference generated by printing of the electrode
pattern layer 12a, the electrode level difference
absorbing dielectric paste was used to form a dielectric
blank pattern layer 24 on level difference gap portions
excepting portions corresponding to the electrode

pattern by screen printing. A thickness of the

dielectric blank pattern layer 24 after drying was 1.5µm.
[0079]

Next, the dielectric green sheets 10a provided with the electrode pattern layer 12a and dielectric blank pattern layer 24 are stacked by 300 layers while aligning to attain a structure, wherein the electrode pattern layers 12 are formed alternately, so that a multilayer body 30 was obtained. On top and bottom of the multilayer body 30 were sandwiched by external sheet formed only by a dielectric having a thickness of 100µm, pressed and cut to obtain a ceramic green multilayer chip.

[0080]

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Evaluation on Characteristics

15 The obtained chip exhibited no unsticking between layers and cutting defects. The chip was subjected to binder removal processing and firing by following a normal method so as to obtain a capacitor element body. By providing external electrodes on both end surfaces of the capacitor element body, a multilayer ceramic capacity sample was completed. The obtained sample chip was checked whether any breakings, cracks, unsticking between layers inside and structure damages were caused by an outside inspection and cut surface observance.

25 Their internal defective rate was 0% and the interlayer

thickness was 2.2µm. the results are shown together in Table 1.

[0081]

[Table 1]

	ï					i												
		(Sheet)	(Ē	Ex. 1 (Blank 1)	k 1)	Ex. 2	Ex. 2 (Blank 2)	(2)	Ā.	Ex. 3 (Blank 3)	k 3)	Ex.	Ex. 4 (Blank 4)	k 4)	Ex :	Ex 5 (Blank 5)	ر ق
	냚	W.C.S.	wt% vol?	phr	wts	Vol9	훒	wts vols	vol	phr	wt%	volg	phr	wt8	Vo所	phr	Wt.R	別
norganio Colorant		100	18.87		100	16.7		100	18.7		100	18.7		100	16.7		100	18.7
Resin		5	8		4.5	4,5		4.5	4.5		8	8		8	9		6	8
Jasticizar	8	3	3	140	6.3	6.3	250	11.3	11.3	8	4.8	4.8	100	θ	8	30	1.7	2.7
Organic Binder Component		6	6		10.8	10.8		15.8	15.8		10.8	10.8		12	12		11.7	11.7
									1				Π		Ī		1	T
horganic Colorant Ratio of Resin and Plasticizer		đ	35		10.8	64.8		15.8	94.5		10.8	10.8 64.8		12	72		11.7 70.2	70.2
Difference of Inorganic Colorant Ratio: (Blank) – (Sheet)	Colora eet)	nt Rati	ö		1.8	10.8		6.75 40.5	40.5		1.8	10.8		3	18	•	2.7	16.2
				Eth	Ethyl Cellulose	lose	Ethy	Ethyl Cellulose	980		Butyral			Butyral			Butyral	
Property																		Ì
Stacking Property of	rty of			애	No Unsticking	king	₽	No Unsticking	ing	2	No Unsticking	ding	No	No Unsticking	cing	€	No Unsticking	ding
Ceramic Green Multilayer Chip	Jiayer	읁		betw	between Layers	yers	betw	between Layers	yers	betw	between Layers	yers	betw	between Layers	yers	betw	between Layers	yers
Cutting Yield %	% P				1 00			100 100			100			100			5	
Internal Defect Rate % of Ceramic Capacitor Samples	late %	of les			0			0			0			0			0	
Irterlaver Thickness If m	1 290	E			22			2.1			22			91			2.2	

[0082]

[Table 2]

								ĺ							
		(Sheet)	2	පු ප	Comp. Ex. (Blank 6)	1.8 1.	8 8	Comp. Ex 2 (Blank 7)	2.(8 8	Comp. Ex. 3 (Blank 8)	6	23	Ref. Ex. 1 (Blank 9)	
	phr	wts	※04	phr	1 3	WES VOIN	븁	\$Ç	岩	휺	wt8	<u>%</u>	ķ	No.	E S
Increanic Colorant		100	16.67		100	18.7		100	2	T	٤	Ę		٤	
Resin			۵		6	6		_	Ę	T	•	3		3 5	
Plasticizer	22	6	~	٩	٥	٠	Ę	3 5	1	٤	7	- :	ŀ	≥	2
Ormanic Binder Comment		,	,	-	•]•	3	३	3	3	8	2	.,5	0	0	0
Organio on ruer component	1	3	-		6	6		5	5.4		7.5	7.5		10	10
Inorganic Colorant Ratio of Resin and Plasticizer		\$	盂		6	54		5.4	32.4		7.5	45		2	99
Difference of Inorganic Colorant Ratio: (Blank) – (Sheet)	a Series	r Pati	ь		0	0		86	27		7.7.	9		-	9
Prenado					Butyral			Ethyl Cellukose	8	圕	Ethyl Cellulose	980]]	Butyral	
Chiedal															
Stacking Property of Geramio Green Multilayer Chip	ty of layer C	泽		betsw Freque	Unsticking between Layers Frequently Arises	Vers	Unsticking Unsticking between Layers between Layers Frequently Arises	Unsticking tween Laye	g fers		Ursticking between Layers Partially	, <u>à</u>	No C betw	No Unsticking between Layers	ing /ers
Cutting Yield %	35				1	T		,			Arisas 50		İ	¥.	
Internal Defect Pate K of Ceramic Capacitor Samples	Sample	<u></u>			1			1			2 2			\$ \$	
Interlayer Thiokness μ_{m}	38 µn	اے			1				T		1	T		lı	

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Other than changing an amount of the plasticizer

[0083]

Example 2

to 11.3 parts by weight in the electrode level

5 difference absorbing dielectric paste, multilayer
ceramic capacitor samples were produced in the same way
as that in the example 1. In this case, the plasticizer
amount in the dielectric paste was 250 wt% with respect
to the polymeric resin. Also, the ratio of the organic

10 binder component was 15.8 wt% with respect to the
dielectric inorganic colorant powder, which was larger
than the organic binder component ratio of 9 wt% in the
dielectric slurry by 6.8 wt%.

[0084]

15 The obtained ceramic green chip multilayer chip exhibited no unsticking between layers and cutting defects in the same way as in the example 1. Also, internal defects were not observed in the sample chip obtained after firing as in the example 1, and the 20 interlayer thickness was 2.1µm. The results are listed in Table 1.

[0085]

Example 3

Other than using 6 parts by weight of a butyral 25 resin as a polymeric resin and 4.8 parts by weight of

dioctyl phthalate (DOP) as a plasticizer in the
electrode level difference absorbing dielectric paste,
multilayer ceramic capacitor sample was produced in the
same way as that in the example 1. In this case, an

5 amount of the plasticizer in the dielectric paste was 80
wt% with respect to the polymeric resin. Also, a ratio
of the organic binder component was 10.8 wt% with
respect to the dielectric inorganic colorant powder,
which was larger than the organic binder component ratio
10 of 9 wt% in the dielectric slurry by 1.8 wt%.

[0086]

The obtained ceramic green multilayer chip exhibited no unsticking between layers and cutting defects in the same way as in the example 1. also, the interlayer thickness of the sample chip obtained after firing was 2.2µm, and no internal defects were observed. The results are listed in Table 1.

[0087]

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Example 4

By using the same green sheet slurry as that in the example 1, a dielectric green sheet having a thickness of 2.4µm after drying was produced. Also, other than changing the polymeric resin to 6 parts by weight of a butyral resin and the plasticizer to 6 parts by weight of dioctyl phthalate (DOP), an electrode level

difference absorbing dielectric paste was produced in the same way as that in the example 1.

[0088]

Other than using the dielectric green sheet and
the electrode level difference absorbing dielectric
paste, multilayer ceramic capacitor sample was produced
in the same way as that in the example 1. In this case,
an amount of the plasticizer in the dielectric paste was
100 wt% with respect to the polymeric resin. Also, a
ratio of the organic binder component was 12 wt% with
respect to the dielectric inorganic colorant powder,
which was larger than the organic binder component ratio
of 9 wt% in the dielectric slurry by 3 wt%.

[0089]

15 The obtained ceramic green multilayer chip exhibited no unsticking between layers and cutting defects in the same way as in the example 1. Also, the interlayer thickness of the sample chip obtained after firing was 1.6µm, and no internal defects were observed.

20 The results are listed in Table 1.

[0090]

Example 5

Other than using 9 parts by weight of a butyral resin as a polymeric resin and 2.7 parts by weight of dioctyl phthalate (DOP) as a plasticizer in the

electrode level difference absorbing dielectric paste, multilayer ceramic capacitor sample was produced in the same way as that in the example 1. In this case, an amount of the plasticizer in the dielectric paste was 30 wt% with respect to the polymeric resin. Also, a ratio of the organic binder component was 11.7 wt% with respect to the dielectric inorganic colorant powder, which was larger than the organic binder component ratio of 9 wt% in the dielectric slurry by 2.7 wt%.

10 [0091]

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The obtained ceramic green multilayer chip exhibited no unsticking between layers and cutting defects in the same way as in the example 1. Also, the interlayer thickness of the sample chip obtained after firing was 2.2µm, and no internal defects were observed. The results are listed in Table 1.

[0092]

Comparative Example 1

Other than not adding a plasticizer and changing

the polymer resin to 9 parts by weight of a polyvinyl
butyral resin in the electrode level difference
absorbing dielectric paste, an electrode layer and a
dielectric blank pattern layer were formed on the
dielectric green sheet as in the same way as that in the

example 1.

[0093]

In this case, a plasticizer was not included in the electrode level difference absorbing dielectric paste, and a ratio of the organic binder component in the electrode level difference absorbing dielectric paste was 9 wt% with respect to the dielectric inorganic colorant powder, which was the same ratio as the organic binder component ratio of 9 wt% in the dielectric slurry.

[0094]

10 In the same way as in the example 1, the dielectric green sheets provided with the electrode layer and dielectric blank pattern layer were stacked, pressed and cut, but adhesiveness between the electrode and level difference absorbing surface and dielectric sheet surface was weak and unsticking between layers frequently occurred after cutting, so that a multilayer chip was not able to be obtained. The results are listed in Table 2.

[0095]

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20 Comparative Example 2

Other than changing an amount of the plasticizer to 0.9 parts by weight in the electrode level difference absorbing dielectric paste, an electrode layer and a dielectric blank pattern layer were formed on the dielectric green sheet as in the same way as that in the

example 1.

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[0096]

In this case, the amount of the plasticizer was 20 parts by weight with respect to the polymeric resin in the electrode level difference absorbing dielectric paste, and a ratio of the organic binder component was 5.4 wt% with respect to the dielectric inorganic colorant powder, which was smaller than the organic binder component ratio of 9 wt% in the dielectric slurry by 3.6 wt%.

[0097]

In the same way as in the example 1, the dielectric green sheets provided with the electrode layer and dielectric blank pattern layer were stacked, pressed and cut, but adhesiveness between the electrode and level difference absorbing surface and dielectric sheet surface was weak and unsticking between layers frequently occurred after cutting, so that a multilayer chip was not able to be obtained. The results are listed in Table 2.

[8000]

Comparative Example 3

Other than changing the polymeric resin to 6 parts by weight of an ethyl cellulose resin and the plasticizer to 1.5 parts by weight of benzylbutyl

phthalate (BBP) in the electrode level difference absorbing dielectric paste, an electrode layer and a dielectric blank pattern layer were formed on the dielectric green sheet as in the same way as that in the example 1.

[0099]

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In this case, the amount of the plasticizor was 25 wt% with respect to the polymeric resin in the electrode level difference absorbing dielectric paste, and a ratio of the organic binder component was 7.5 wt% with respect to the dielectric inorganic colorant powder, which was smaller than the organic binder component ratio of 9 wt% in the dielectric slurry by 1.5 wt%.

[0100]

In the same way as in the example 1, the dielectric green sheets provided with the electrode layer and dielectric blank pattern layer were stacked, pressed and cut, but there were chips exhibiting unsticking between layers, and the cutting yield was 50%.

The internal defect rate in the samples fired in the same way as in the example 1 was 50%. The results are listed in Table 2.

[0101]

Reference Example 1

25 Other than not adding a plasticizer and changing

the polymer resin to 10 parts by weight of a polyvinyl butyral resin in the electrode level difference absorbing dielectric paste, an electrode layer and a dielectric blank pattern layer were formed on the dielectric green sheet in the same way as that in the example 1.

[0102]

In this case, a plasticizer was not included in the electrode level difference absorbing dielectric

10 paste, and a ratio of the organic binder component in the electrode level difference absorbing dielectric paste was 10 wt% with respect to the dielectric inorganic colorant powder, which was larger than the organic binder component ratio of 9 wt% in the

15 dielectric slurry by 1 wt%.

[0103]

In the same way as in the example 1, the dielectric green sheets provided with the electrode layer and dielectric blank pattern layer were stacked,

20 pressed and cut. The cutting yield was 85% and the stacking property was preferable, but the shrinkage rate in the firing step was large due to a large amount of resin, and the internal defect rate was 40% in the samples fired in the same way as in the example 1. The

25 results are listed in Table 2

[0104]

By comparing the reference example 1 with the example 1, it was confirmed that B-A ≥ 1.5 was preferable when assuming that the rate of the organic 5 binder component with respect to the dielectric inorganic colorant powder in the green sheet slurry was "A" and the ratio of the organic binder component with respect to the dielectric inorganic colorant powder in the electrode level difference absorbing dielectric paste was "B".

[0105]

As explained above, according to the present invention, it is possible to produce a multilayer ceramic device, such as a multilayer ceramic capacitor having a large capacity, wherein the interlayer thickness is made as thin as about 2.5µm or thinner, at a high production yield without causing unsticking between layers and internal defects.